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J1639 U.S. PTO
09/09/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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J1000 U.S. PTO
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Patent Application of: Frank O'Mahony et al.

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS

Attorney Docket No.: 884.405US1

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- X Return postcard.
- X Utility Patent Application under 37 CFR § 1.53(b) comprising:
 - X Specification (13 pgs, including claims numbered 1 through 25 and a 1 page Abstract).
 - X Formal Drawing(s) (4 sheets).
 - X **Signed Combined Declaration and Power of Attorney** (8 pgs).
 - X Check in the amount of \$800.00 to pay the filing fee.
- ✓X Assignment of the invention to Intel Corporation (4 pgs) and Recordation Form Cover Sheet.
- X Check in the amount of \$40.00 to pay the Assignment recording fee.

The filing fee has been calculated below as follows:

| | No. Filed | No. Extra | Rate | Fee |
|-----------------------------------------|-----------|-----------|--------|----------|
| TOTAL CLAIMS | 25 - 20 = | 5 | x 18 = | \$90.00 |
| INDEPENDENT CLAIMS | 3 - 3 = | 0 | x 80 = | \$0.00 |
| [] MULTIPLE DEPENDENT CLAIMS PRESENTED | | | | \$0.00 |
| BASIC FEE | | | | \$710.00 |
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This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.